Features and Benefits

- Wide operating voltage range from 3.5V to 24V
- High magnetic sensitivity – Multi-purpose
- CMOS technology
- Chopper-stabilized amplifier stage
- Low current consumption
- Open drain output
- Thin SOT23 3L and flat TO-92 3L both RoHS Compliant packages

Application Examples

- Automotive, Consumer and Industrial
- Solid-state switch
- Brushless DC motor commutation
- Speed detection
- Linear position detection
- Angular position detection
- Proximity detection

Ordering Code

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>US1881</td>
<td>E</td>
<td>UA</td>
<td>AAA-000</td>
<td>BU</td>
</tr>
<tr>
<td>US1881</td>
<td>K</td>
<td>UA</td>
<td>AAA-000</td>
<td>BU</td>
</tr>
<tr>
<td>US1881</td>
<td>L</td>
<td>UA</td>
<td>AAA-000</td>
<td>BU</td>
</tr>
<tr>
<td>US1881</td>
<td>E</td>
<td>SE</td>
<td>AAA-000</td>
<td>RE</td>
</tr>
<tr>
<td>US1881</td>
<td>K</td>
<td>SE</td>
<td>AAA-000</td>
<td>RE</td>
</tr>
<tr>
<td>US1881</td>
<td>L</td>
<td>SE</td>
<td>AAA-000</td>
<td>RE</td>
</tr>
</tbody>
</table>

Legend:

Temperature Code:
- L for Temperature Range -40°C to 150°C
- E for Temperature Range -40°C to 85°C
- K for Temperature Range -40°C to 125°C

Package Code:
- UA for TO-92(Flat), SE for TSOT

Option Code:
- xxx-000: Standard version

Packing Form Code:
- RE for Reel, BU for Bulk

Ordering example: US1881KUA-AAA-000-BU

1 Functional Diagram

2 General Description

The Melexis US1881 is a Hall-effect latch designed in mixed signal CMOS technology.

The device integrates a voltage regulator, Hall sensor with dynamic offset cancellation system, Schmitt trigger and an open-drain output driver, all in a single package.

Thanks to its wide operating voltage range and extended choice of temperature range, it is quite suitable for use in automotive, industrial and consumer applications.

The device is delivered in a Thin Small Outline Transistor (TSOT) for surface mount process and in a Plastic Single In Line (TO-92 flat) for through-hole mount.
Both 3-lead packages are RoHS compliant.
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3 Glossary of Terms

MilliTesla (mT), Gauss  Units of magnetic flux density:
1 mT = 10 Gauss

RoHS  Restriction of Hazardous Substances

TSOT  Thin Small Outline Transistor (TSOT package) – also referred with the Melexis package code “SE”

ESD  Electro-Static Discharge

BLDC  Brush-Less Direct-Current

Operating Point (B_OP)  Magnetic flux density applied on the branded side of the package which turns the output driver ON (V_{OUT} = V_{DSon})

Release Point (B_RP)  Magnetic flux density applied on the branded side of the package which turns the output driver OFF (V_{OUT} = high)

4 Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Value</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>V_{DD}</td>
<td>28</td>
<td>V</td>
</tr>
<tr>
<td>Supply Current</td>
<td>I_{DD}</td>
<td>50</td>
<td>mA</td>
</tr>
<tr>
<td>Output Voltage</td>
<td>V_{OUT}</td>
<td>28</td>
<td>V</td>
</tr>
<tr>
<td>Output Current</td>
<td>I_{OUT}</td>
<td>50</td>
<td>mA</td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>T_s</td>
<td>-50 to 150</td>
<td>°C</td>
</tr>
<tr>
<td>Maximum Junction Temperature</td>
<td>T_J</td>
<td>165</td>
<td>°C</td>
</tr>
</tbody>
</table>

Table 1: Absolute maximum ratings

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

<table>
<thead>
<tr>
<th>Operating Temperature Range</th>
<th>Symbol</th>
<th>Value</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Temperature Suffix “E”</td>
<td>T_a</td>
<td>-40 to 85</td>
<td>°C</td>
</tr>
<tr>
<td>Temperature Suffix “K”</td>
<td>T_a</td>
<td>-40 to 125</td>
<td>°C</td>
</tr>
<tr>
<td>Temperature Suffix “L”</td>
<td>T_a</td>
<td>-40 to 150</td>
<td>°C</td>
</tr>
</tbody>
</table>

5 Pin Definitions and Descriptions

<table>
<thead>
<tr>
<th>SE Pin №</th>
<th>UA Pin №</th>
<th>Name</th>
<th>Type</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1</td>
<td>VDD</td>
<td>Supply</td>
<td>Supply Voltage pin</td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td>OUT</td>
<td>Output</td>
<td>Open Drain Output pin</td>
</tr>
<tr>
<td>3</td>
<td>2</td>
<td>GND</td>
<td>Ground</td>
<td>Ground pin</td>
</tr>
</tbody>
</table>

Table 2: Pin definitions and descriptions
6 General Electrical Specifications

DC Operating Parameters $T_A = 25^\circ C$, $V_{DD} = 3.5V$ to 24V (unless otherwise specified)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Test Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage $V_{DD}$</td>
<td>$V_{DD}$</td>
<td>Operating</td>
<td>3.5</td>
<td>24</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Supply Current $I_{DD}$</td>
<td>$I_{DD}$</td>
<td>$B &lt; B_{RP}$</td>
<td>5</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>Output Saturation Voltage</td>
<td>$V_{DSon}$</td>
<td>$I_{OUT} = 20mA$, $B &gt; B_{OP}$</td>
<td>0.5</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Output Leakage Current $I_{OFF}$</td>
<td>$I_{OFF}$</td>
<td>$B &lt; B_{RP}$, $V_{OUT} = 24V$</td>
<td>0.3</td>
<td>10</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>Output Rise Time $t_r$</td>
<td>$t_r$</td>
<td>$R_L = 1k\Omega$, $C_L = 20pF$</td>
<td>0.25</td>
<td></td>
<td></td>
<td>μs</td>
</tr>
<tr>
<td>Output Fall Time $t_f$</td>
<td>$t_f$</td>
<td>$R_L = 1k\Omega$, $C_L = 20pF$</td>
<td>0.25</td>
<td></td>
<td></td>
<td>μs</td>
</tr>
<tr>
<td>Maximum Switching Frequency</td>
<td>$F_{SW}$</td>
<td></td>
<td>10</td>
<td></td>
<td></td>
<td>kHz</td>
</tr>
<tr>
<td>Package Thermal Resistance $R_{TH}$</td>
<td>$R_{TH}$</td>
<td>Single layer (1S) Jedec board</td>
<td>301</td>
<td></td>
<td></td>
<td>°C/W</td>
</tr>
</tbody>
</table>

Table 3: Electrical specifications

7 Magnetic Specifications

DC Operating Parameters $V_{DD} = 3.5V$ to 24V (unless otherwise specified)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Test Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating Point $B_{OP}$</td>
<td>$B_{OP}$</td>
<td>E spec., $T_A = 85^\circ C$</td>
<td>0.5</td>
<td>9.5</td>
<td></td>
<td>mT</td>
</tr>
<tr>
<td>Release Point $B_{RP}$</td>
<td>$B_{RP}$</td>
<td></td>
<td>-9.5</td>
<td>-0.5</td>
<td></td>
<td>mT</td>
</tr>
<tr>
<td>Hysteresis $B_{HYST}$</td>
<td>$B_{HYST}$</td>
<td></td>
<td>7</td>
<td>12</td>
<td></td>
<td>mT</td>
</tr>
<tr>
<td>Operating Point $B_{OP}$</td>
<td>$B_{OP}$</td>
<td>K spec., $T_A = 125^\circ C$</td>
<td>0.5</td>
<td>9.5</td>
<td></td>
<td>mT</td>
</tr>
<tr>
<td>Release Point $B_{RP}$</td>
<td>$B_{RP}$</td>
<td></td>
<td>-9.5</td>
<td>-0.5</td>
<td></td>
<td>mT</td>
</tr>
<tr>
<td>Hysteresis $B_{HYST}$</td>
<td>$B_{HYST}$</td>
<td></td>
<td>7</td>
<td>12</td>
<td></td>
<td>mT</td>
</tr>
<tr>
<td>Operating Point $B_{OP}$</td>
<td>$B_{OP}$</td>
<td>L spec., $T_A = 150^\circ C$</td>
<td>0.5</td>
<td>9.5</td>
<td></td>
<td>mT</td>
</tr>
<tr>
<td>Release Point $B_{RP}$</td>
<td>$B_{RP}$</td>
<td></td>
<td>-9.5</td>
<td>-0.5</td>
<td></td>
<td>mT</td>
</tr>
<tr>
<td>Hysteresis $B_{HYST}$</td>
<td>$B_{HYST}$</td>
<td></td>
<td>6</td>
<td>12.5</td>
<td></td>
<td>mT</td>
</tr>
</tbody>
</table>

Table 4: Magnetic specifications

Note 1: For typical values, please refer to the performance graphs in section 11

8 Output Behaviour versus Magnetic Pole

DC Operating Parameters $T_A = -40^\circ C$ to 150°C, $V_{DD} = 3.5V$ to 24V (unless otherwise specified)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions (SE)</th>
<th>OUT (SE)</th>
<th>Test Conditions (UA)</th>
<th>OUT (UA)</th>
</tr>
</thead>
<tbody>
<tr>
<td>South pole</td>
<td>$B &lt; B_{RP}$</td>
<td>High</td>
<td>$B &gt; B_{OP}$</td>
<td>Low</td>
</tr>
<tr>
<td>North pole</td>
<td>$B &gt; B_{OP}$</td>
<td>Low</td>
<td>$B &lt; B_{RP}$</td>
<td>High</td>
</tr>
</tbody>
</table>

Table 5: Output behaviour versus magnetic pole
9 Detailed General Description

Based on mixed signal CMOS technology, Melexis US1881 is a Hall-effect device with high magnetic sensitivity. This multi-purpose latch suits most of the application requirements.

The chopper-stabilized amplifier uses switched capacitor technique to suppress the offset generally observed with Hall sensors and amplifiers. The CMOS technology makes this advanced technique possible and contributes to smaller chip size and lower current consumption than bipolar technology. The small chip size is also an important factor to minimize the effect of physical stress. This combination results in more stable magnetic characteristics and enables faster and more precise design.

The wide operating voltage from 3.5V to 24V, low current consumption and large choice of operating temperature range according to “L”, “K” and “E” specification make this device suitable for automotive, industrial and consumer applications.

The output signal is open-drain type. Such output allows simple connectivity with TTL or CMOS logic by using a pull-up resistor tied between a pull-up voltage and the device output.

10 Unique Features

The US1881 exhibits latch magnetic switching characteristics. Therefore, it requires both south and north poles to operate properly.

The device behaves as a latch with symmetric operating and release switching points (\(B_{OP}=|B_{RP}|\)). This means magnetic fields with equivalent strength and opposite direction drive the output high and low.

Removing the magnetic field (\(B\rightarrow0\)) keeps the output in its previous state. This latching property defines the device as a magnetic memory.

A magnetic hysteresis \(B_{Hyst}\) keeps \(B_{OP}\) and \(B_{RP}\) separated by a minimal value. This hysteresis prevents output oscillation near the switching point.
11 Performance Graphs

11.1 Magnetic parameters vs. $T_A$

11.2 Magnetic parameters vs. $V_{DD}$

11.3 $V_{DSon}$ vs. $T_A$

11.4 $V_{DSon}$ vs. $V_{DD}$

11.5 $I_{DD}$ vs. $T_A$

11.6 $I_{DD}$ vs. $V_{DD}$
12 Test Conditions

Note: DUT = Device Under Test

12.1 Supply Current

12.2 Output Saturation Voltage

12.3 Output Leakage Current

12.4 Magnetic Thresholds

Note 1 - The supply current $I_{PP}$ represents the static supply current.
Note 2 - The device is put under magnetic field with B=H$_{BP}$.
13 Application Information

13.1 Typical Three-Wire Application Circuit

13.3 Automotive and Harsh, Noisy Environments Three-Wire Circuit

14 Application Comments

For proper operation, a 100nF bypass capacitor should be placed as close as possible to the device between the V_{DD} and ground pin.

For reverse voltage protection, it is recommended to connect a resistor or a diode in series with the V_{DD} pin. When using a resistor, three points are important:
- the resistor has to limit the reverse current to 50mA maximum (V_{CC} / R \leq 50mA)
- the resulting device supply voltage V_{DD} has to be higher than V_{DD} min (V_{DD} = V_{CC} - R_{DD} I_{DD})
- the resistor has to withstand the power dissipated in reverse voltage condition (P_D = \frac{V_{CC}^2}{R_{DD}})

When using a diode, a reverse current cannot flow and the voltage drop is almost constant (~0.7V). Therefore, a 100\Omega/0.25W resistor for 5V application and a diode for higher supply voltage are recommended. Both solutions provide the required reverse voltage protection.

When a weak power supply is used or when the device is intended to be used in noisy environment, it is recommended that figure 13.3 from the Application Information section is used. The low-pass filter formed by R1 and C1 and the zener diode Z1 bypass the disturbances or voltage spikes occurring on the device supply voltage V_{DD}. The diode D1 provides additional reverse voltage protection.
15 Standard information regarding manufacturability of Melexis products with different soldering processes

Our products are classified and qualified regarding soldering technology, solderability and moisture sensitivity level according to following test methods:

Reflow Soldering SMD’s (Surface Mount Devices)

- IPC/JEDEC J-STD-020
  Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices (classification reflow profiles according to table 5-2)
- EIA/JEDEC JESD22-A113
  Preconditioning of Nonhermetic Surface Mount Devices Prior to Reliability Testing (reflow profiles according to table 2)

Wave Soldering SMD’s (Surface Mount Devices) and THD’s (Through Hole Devices)

- EN60749-20
  Resistance of plastic- encapsulated SMD’s to combined effect of moisture and soldering heat
- EIA/JEDEC JESD22-B106 and EN60749-15
  Resistance to soldering temperature for through-hole mounted devices

Iron Soldering THD’s (Through Hole Devices)

- EN60749-15
  Resistance to soldering temperature for through-hole mounted devices

Solderability SMD’s (Surface Mount Devices) and THD’s (Through Hole Devices)

- EIA/JEDEC JESD22-B102 and EN60749-21
  Solderability

For all soldering technologies deviating from above mentioned standard conditions (regarding peak temperature, temperature gradient, temperature profile etc) additional classification and qualification tests have to be agreed upon with Melexis.

The application of Wave Soldering for SMD’s is allowed only after consulting Melexis regarding assurance of adhesive strength between device and board.

Melexis is contributing to global environmental conservation by promoting lead free solutions. For more information on qualifications of RoHS compliant products (RoHS = European directive on the Restriction Of the use of certain Hazardous Substances) please visit the quality page on our website:

16 ESD Precautions

Electronic semiconductor products are sensitive to Electro Static Discharge (ESD).
Always observe Electro Static Discharge control procedures whenever handling semiconductor products.
17 Package Information

17.1 SE Package (TSOT-3L)

**Notes:**
1. All dimensions are in millimeters.
2. Datum plastic outline width does not include mold flash or protrusions. Mold flash and protrusions shall not exceed 0.15 mm per side.
3. Datum plastic outline length does not include mold flash or protrusions. Mold flash and protrusions shall not exceed 0.15 mm per side.
4. The lead width dimension does not include mold flash protrusion. Allowable mold flash protrusion shall be 0.15 mm total in excess of the lead width dimension at maximum material condition.
5. Dimension B is the length of terminal for soldering to a subcircuit.
6. Dimension on SECTION B-F are apply to the flat section of the lead between 0.10 mm and 0.15 mm from the lead tip.
7. Pinout shall be planar with respect to one another with 0.07 mm at equal pitch.

**Marking:**
Top side: 1 year
1 = part number (US1881)
y = last digit of year
w = calendar week

**Hall plate location**

**Notes:**
1. All dimensions are in millimeters.
17.2 UA Package (TO-92 flat)

Notes:
1. All dimensions are in millimeters
2. Package dimension exclusive molding flash
3. The end flash shall not exceed 0.127 mm on each side.
4. To preserve reliability, it is recommended to have total lead length equal to 2.5mm minimum, measured from the package line.

Marking:
1st Line: UI8 - Part number (US1881)
2nd Line: yww
   y - last digit of year
   ww - calendar week

Hall plate location

Notes:
1. All dimensions are in millimeters
18 Disclaimer

Devices sold by Melexis are covered by the warranty and patent indemnification provisions appearing in its Term of Sale. Melexis makes no warranty, express, statutory, implied, or by description regarding the information set forth herein or regarding the freedom of the described devices from patent infringement. Melexis reserves the right to change specifications and prices at any time and without notice. Therefore, prior to designing this product into a system, it is necessary to check with Melexis for current information. This product is intended for use in normal commercial applications. Applications requiring extended temperature range, unusual environmental requirements, or high reliability applications, such as military, medical life-support or life-sustaining equipment are specifically not recommended without additional processing by Melexis for each application.

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